## Syllabus Form of Academic Discipline Designing devices on microcontrollers and FPGAs. Microcontrollers

No॒	Field name	All the faculties
1.	Name of the faculty	Bachelor's
2.	The level of higher	All the specialties
	education	
3.	Code and title of specialty	Educational Program
4.	The type and title of the	Educational Program of Embedded System in Avionics
	educational program	
5.	Title of the discipline	Designing devices on microcontrollers and FPGAs.
		Microcontrollers
6.	Number of ECTS credits	4
7.	The structure of the course	4 ECTS credits: 12 h. – 6 lecture, 36 h. – 9 laboratory works, 8 h.
	(distribution by type and	- 4 consultations, 64 h. – independent work, type of control:
	hours of training)	exam.
8.	Schedule (terms) of study	3 Course, 5 semester of study
	of the subject	( 2 Course, 3 semester of study for a shortened form of study)
9.	Prerequisites for learning	Disciplines that must be studied before: Higher Mathematics,
	the discipline	Programming,
		Basics of Circuitry, Designing devices on microcontrollers and
		FPGAs. Modeling of digital signals by means of MATLAB and
1.0	A1	VHDL
10.	Abstract (content) of the	Mandatory discipline of basic (professional) training, contains
	discipline	the following content modules:
		Modern STM32 microcontrollers and basics of C language.
		ARM programming of STM32 processors.
11	Compatancias Iznaviladas	Built-in and external peripheral programming.
11.	Competencies, knowledge, skills, understanding that a	- the ability to competently choose the elements of systems: sensors, actuators, digital controllers and to create software;
	higher education acquirer	- be able to justify the choice of technical structure and to
	has in the learning process	develop the application software for microprocessor control
	has in the learning process	systems based on local automation tools, industrial controllers,
		programmable logic matrices and FPGA.
12.	Learning outcomes of a	- develop schematics and write software for such devices as:
	Higher Education applicant	keyboard controller, PWM and analog signal generator, analog
	5	date meter
		digital signal filtering device, UART communication device,
		graphic display control device, etc .;
		- debug software using simulation packages STM32CubeMX and
		IAR Embedded Workbench for ARM;
		- program the microprocessor.
13.	•	To get a positive grade from PPMP. Microcontrollers,
	accordance with each task	students must master three main sections of this course: modern
	for taking tests/exams	STM32 microcontrollers and the basics of the C language, ARM
		programming of STM32 processors, programming of embedded
		and external peripherals.
		Students must complete and defend laboratory work.
		The credit is assessed by a rating, which is defined as the
		number of points obtained by the student during the semester on
		a 100-point scale.

14.	The quality of the	Adherence to the principles of academic integrity
	educational process	http://lib.nure.ua/plagiat, https://nure.ua/branch/akademichna-
		dobrochesnist-ta-zabezpechennja-jakosti-osviti. The laboratory
		workshop is equipped with modern laboratory layouts STM32F4
		DISCOVERY and uses modern software: MatLab,
		STM32CubeMX, IAR Embedded Workbench for ARM v 8.3
		Kikxart X.
15.	Methodological support	Complex of educational and methodical support of
		educational discipline «Designing devices on microcontrollers
		and FPGAs. Microcontrollers» for students of all forms of
		specialties: 125 – «Cybersecurity» (STPI), 151 – «Automation
		and computer-integrated technologies», 152 – «Metrology and
		Information-Measuring Technique», 163 – «Biomedical
		Engineering», 171 – «Electronics», 172 – «Telecommunications
		and radio engineering», 173 – «Avionics» / [Electronic resource]
		Authors.: O. Zubkov, I. Svyd, O. Vorgul. – Kharkiv, 2022. –
		246 p. <a href="http://catalogue.nure.ua/knmz/">http://catalogue.nure.ua/knmz/</a>
16.	The developer of the	Zubkov Oleh, Assosiate Professor of the Department of MTS,
	Syllabus	Candidate of Technical Sciences, Associate Professor,
		oleh.zubkov@nure.ua
		Svyd Iryna, Head of Department of MTS, Candidate of
		Technical Sciences, Associate Professor, iryna.svyd@nure.ua
		Vorgul Oleksander, Assosiate Professor of the Department
		of MTS, Candidate of Technical Sciences, Associate Professor,
		oleksandr.vorgul@nure.ua